Reviewed by: Eric Wagner

Module Name: rtc\_stopwatch

Jira Ticket: SWATCH2-22

**Stopwatch Requirements Checklist Rev. 2**

1. Is requirements document written clearly and grammatically correct?

Yes [ ] No [X]

Comments: INIT\_01 is unclear, ‘in order to’ section is unnecessary.

EX: Module shall preset reset\_n to logic low upon system startup.

1. Does requirements document define all tasks to be performed?

Yes [ ] No [X]

Comments: For RST\_01 define what happens to I/O

Example: The module shall reset all displays to zero when reset\_n is logic high

1. Does requirements document include all inputs and outputs to the module along with their corresponding accuracy, range of values, frequency and format?

Yes [X] No [ ]

Comments:

1. Is each requirement unique?

Yes [X] No [ ]

Comments:

1. Does requirements document comply with system requirements and traceability?

Yes [ ] No [X]

Comments: INST\_02 refers to 20-bit counter instead of 24-bit. Refering to rtc\_name may be easier for

1. Is terminology consistent with standards?

Yes [X] No [ ]

Comments:

1. Is it possible to implement all requirements?

Yes [X] No [ ]

Comments:

1. Is each requirement testable or verifiable?

Yes [X] No [ ]

Comments: